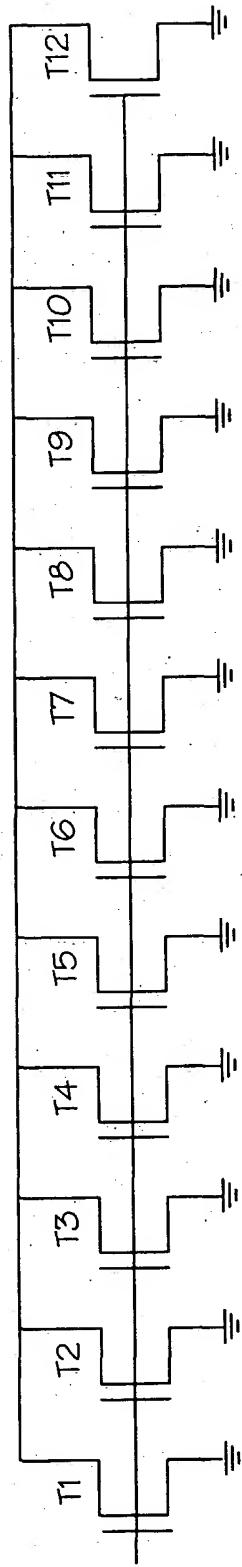


Fig. 1A
(Prior Art)



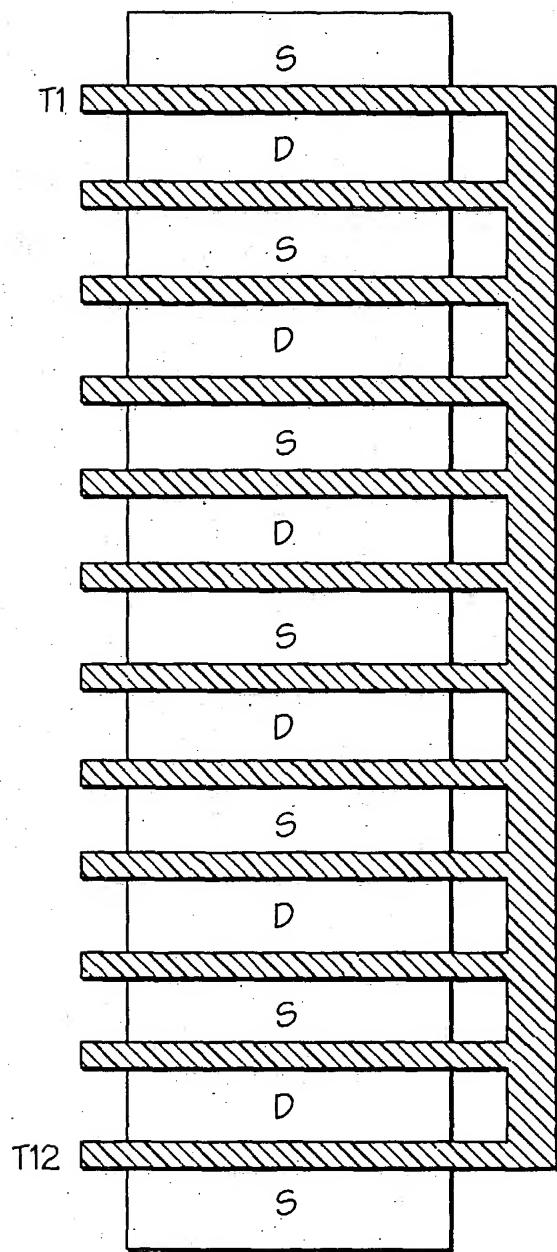
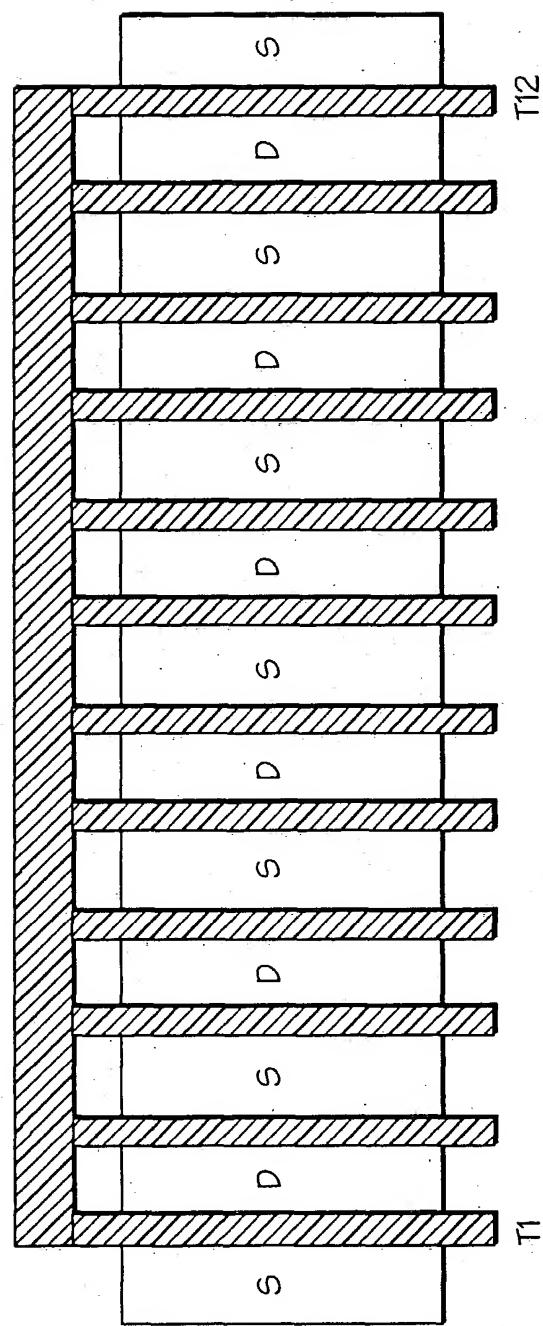


Fig.1B
(Prior Art)

Fig. 1C
(Prior Art)



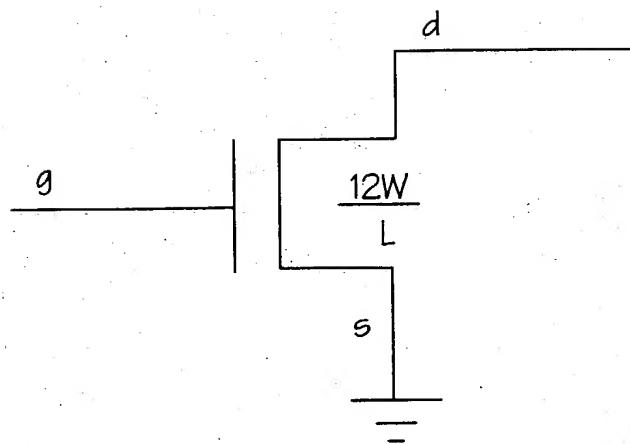


Fig.1D
(Prior Art)

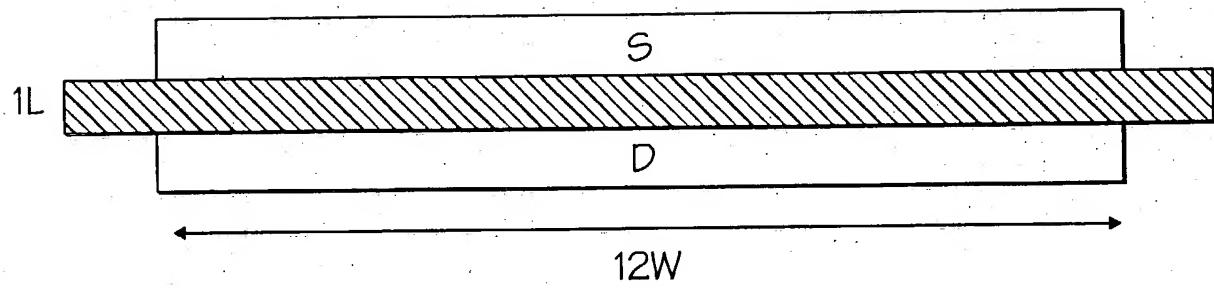
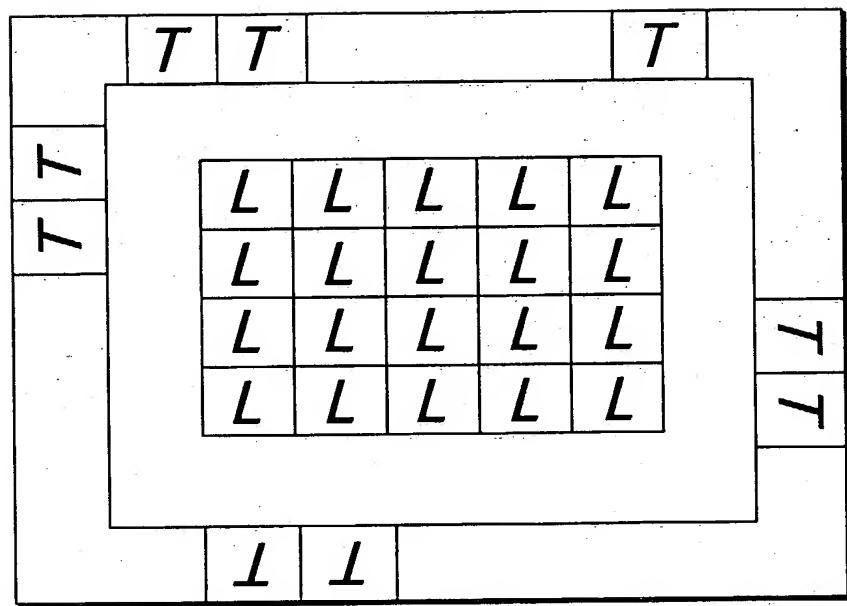


Fig.1E
(Prior Art)



L

Core Cell with **L** orientation mark

T

I/O Cell with **T** orientation mark

Fig.2
(PRIOR ART)

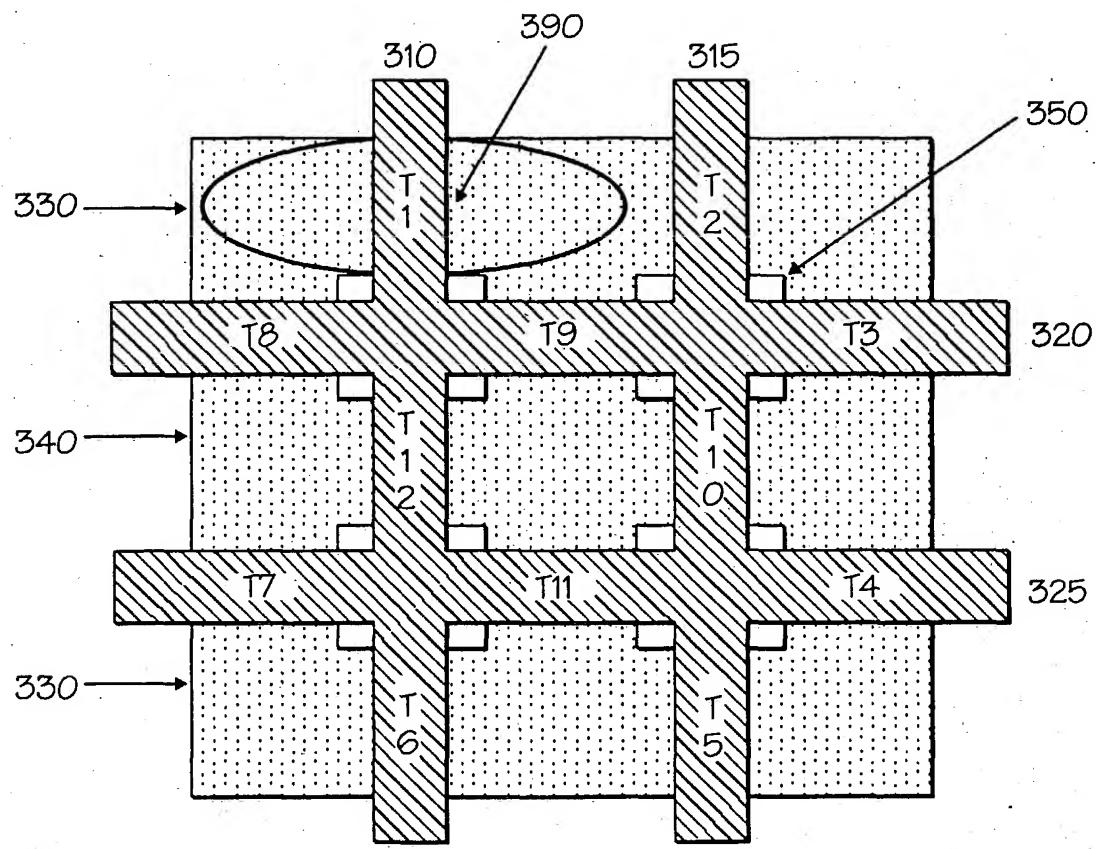


Fig. 3^A

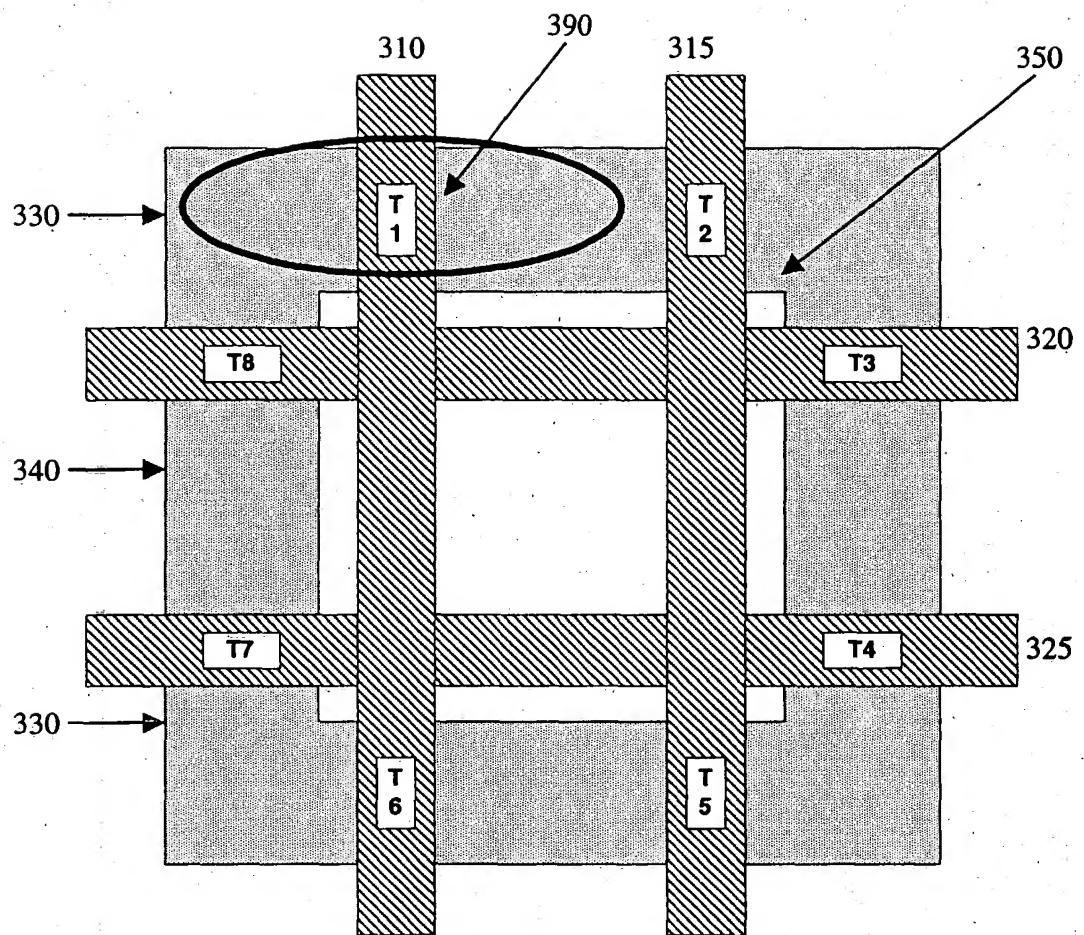


Fig. 3B

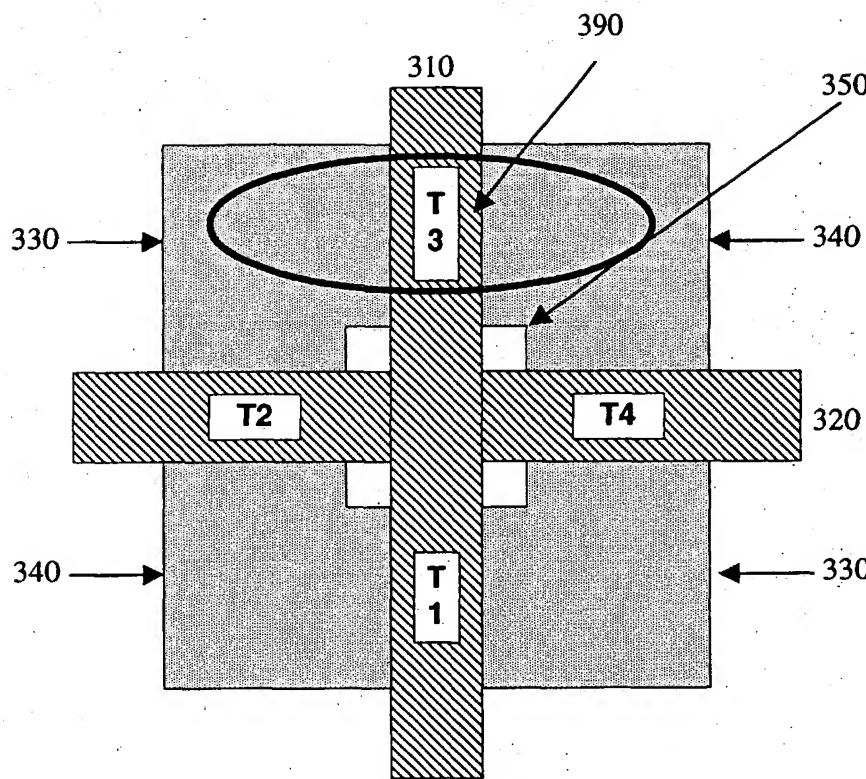
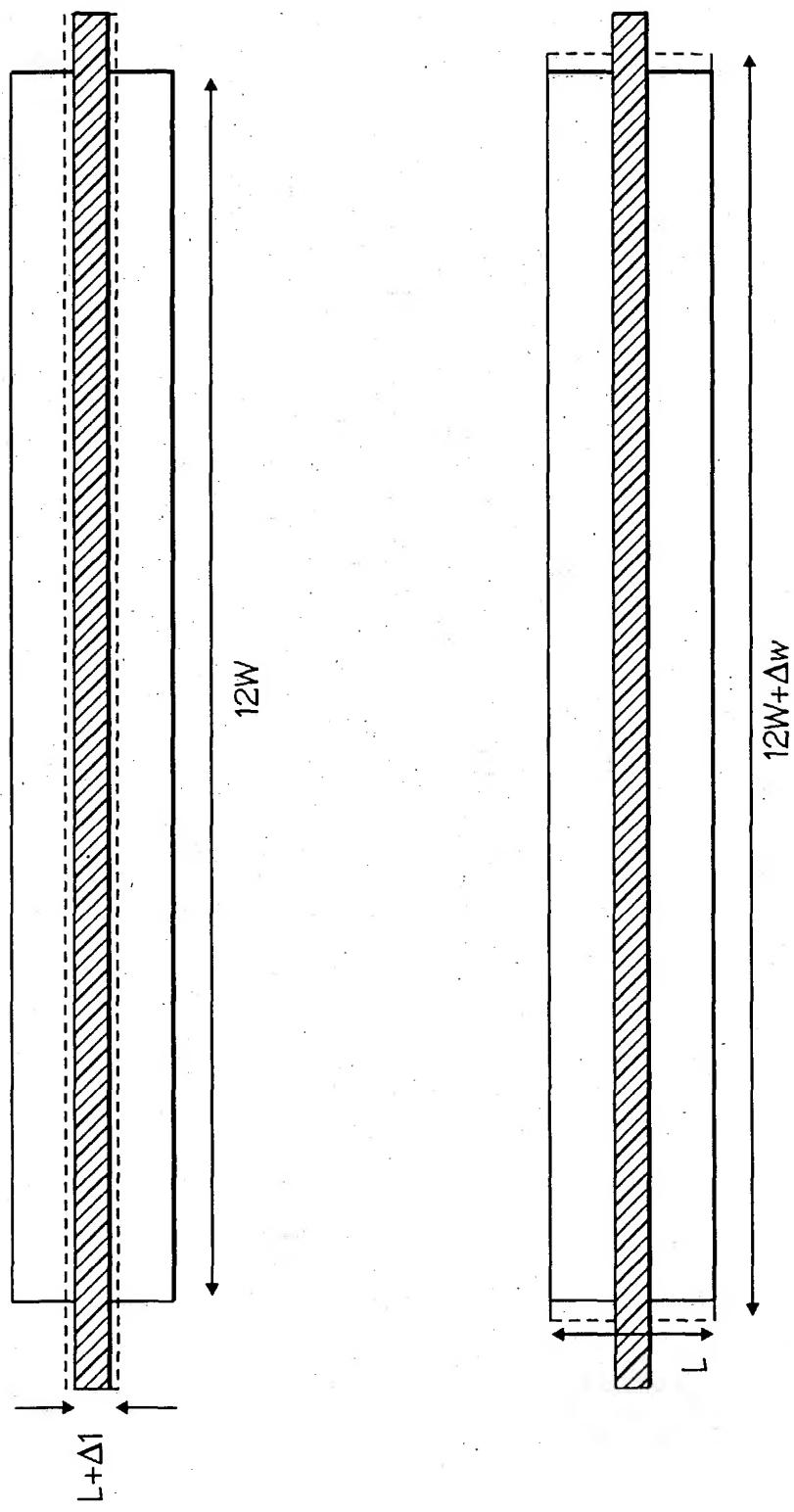
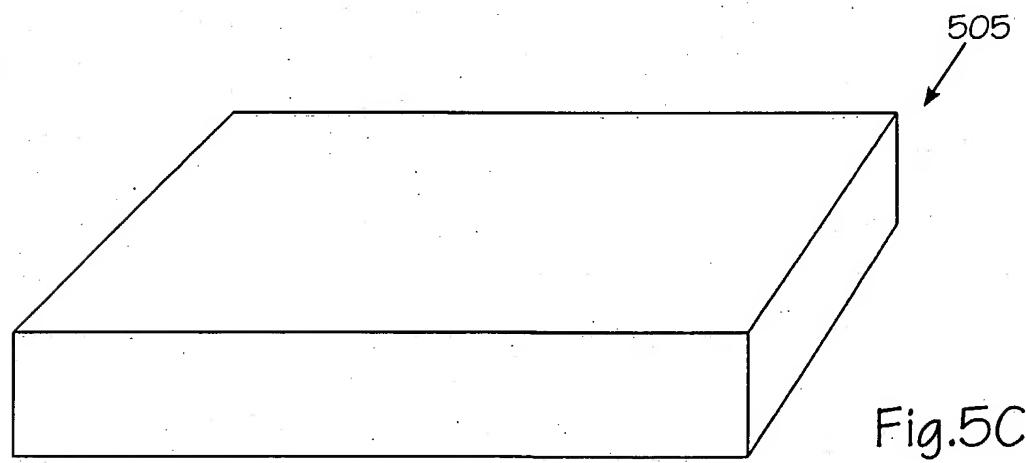
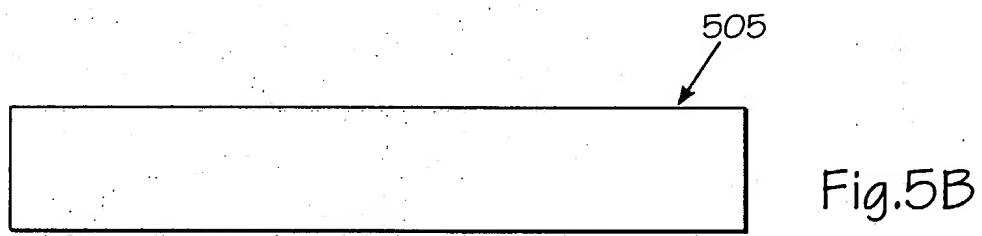
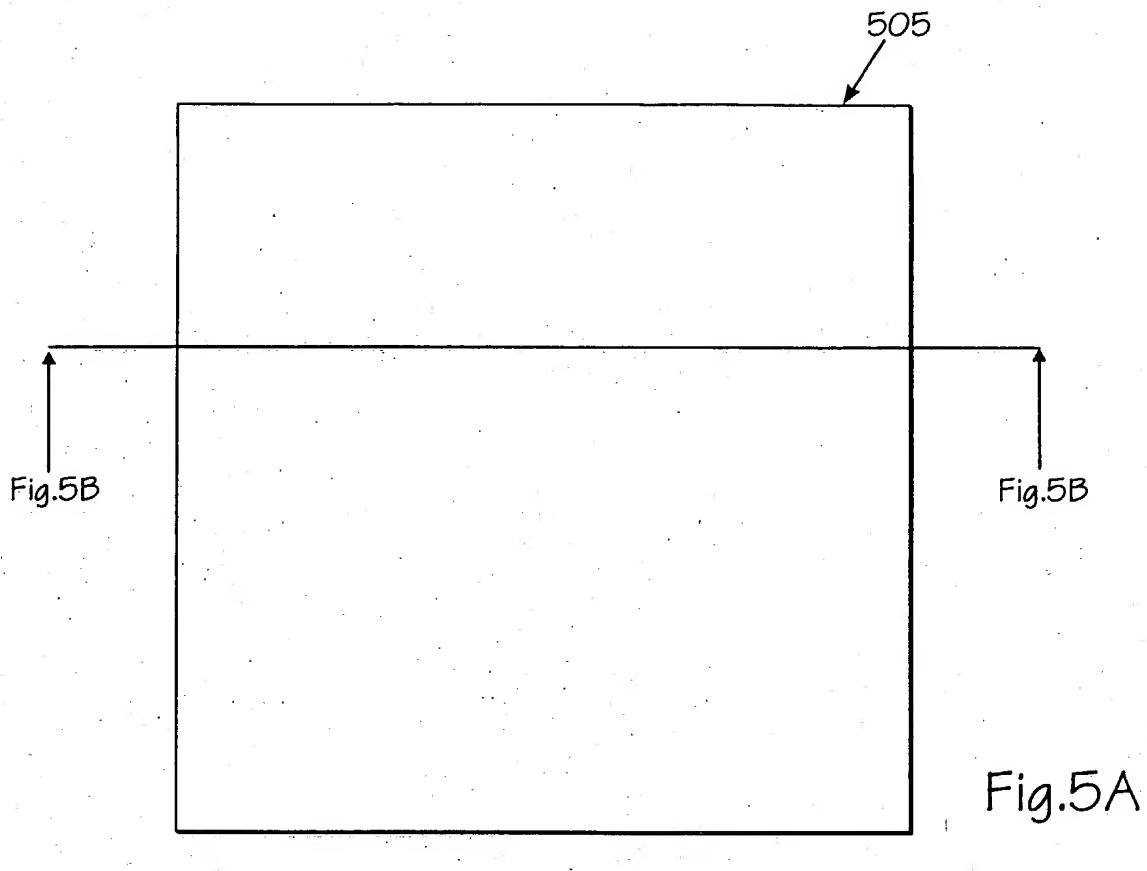


Fig. 3C

Fig. 4





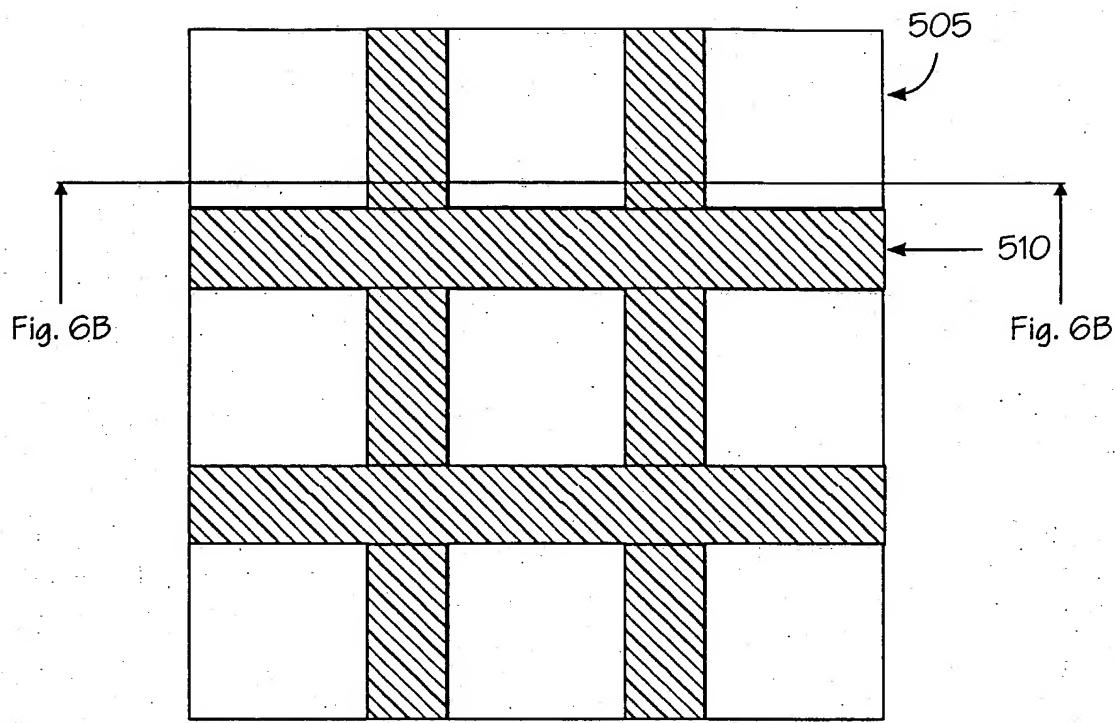


Fig. 6A

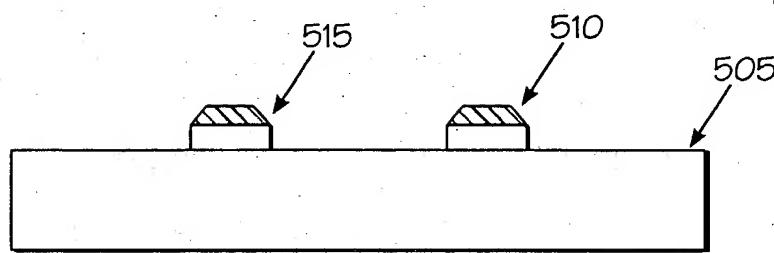


Fig. 6B

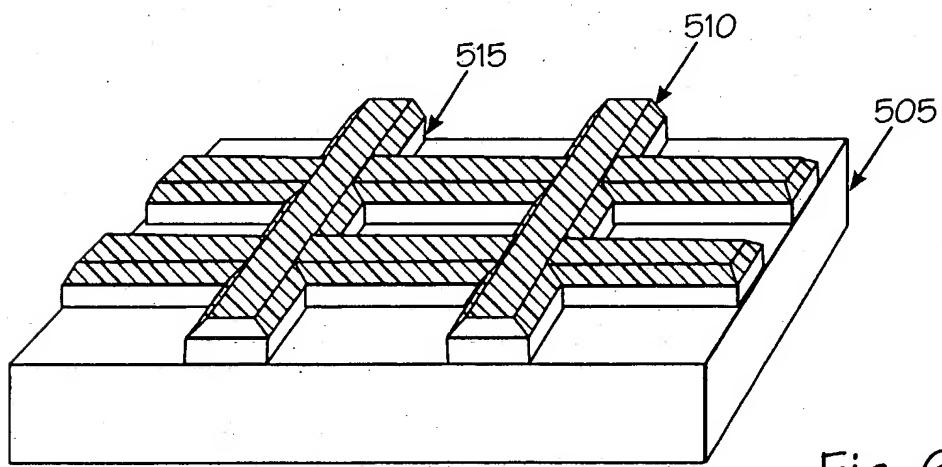


Fig. 6C

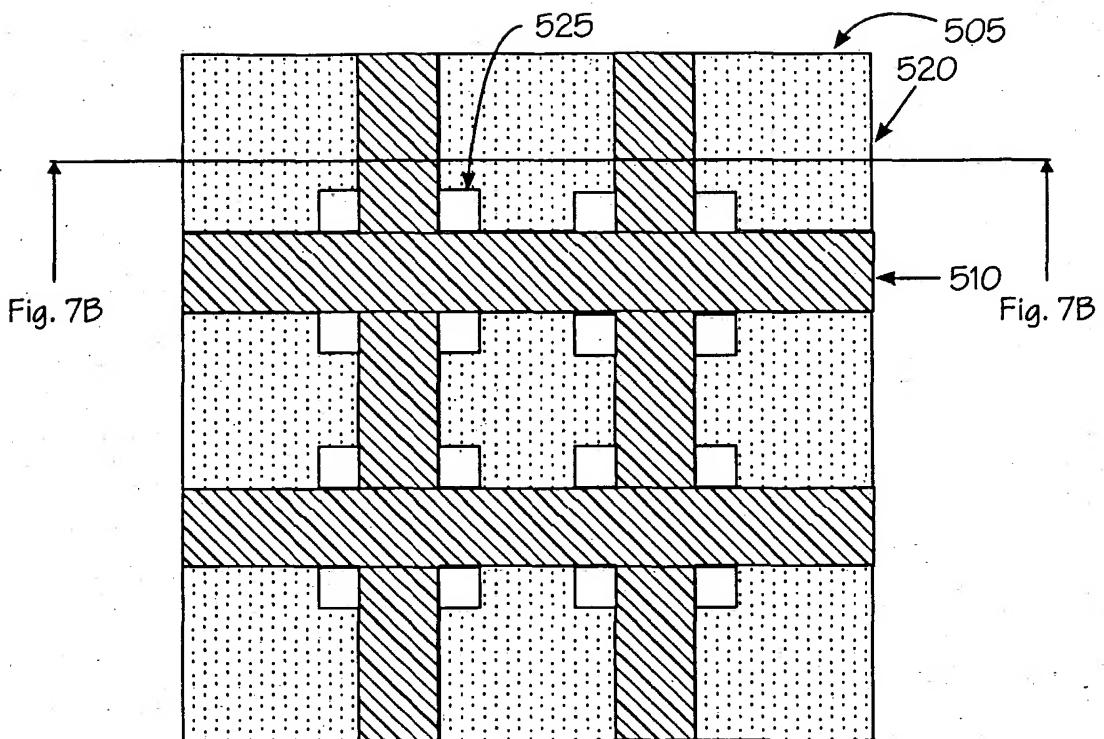


Fig. 7A

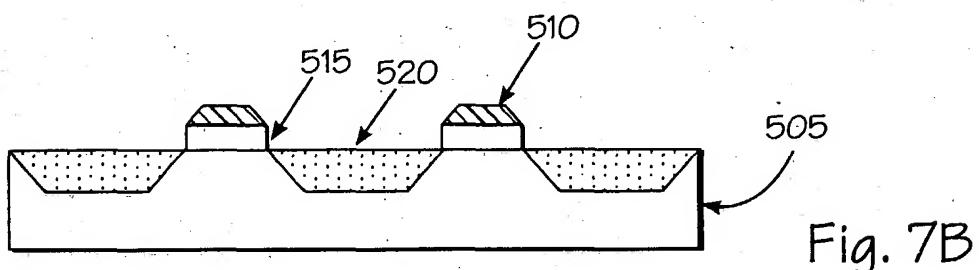


Fig. 7B

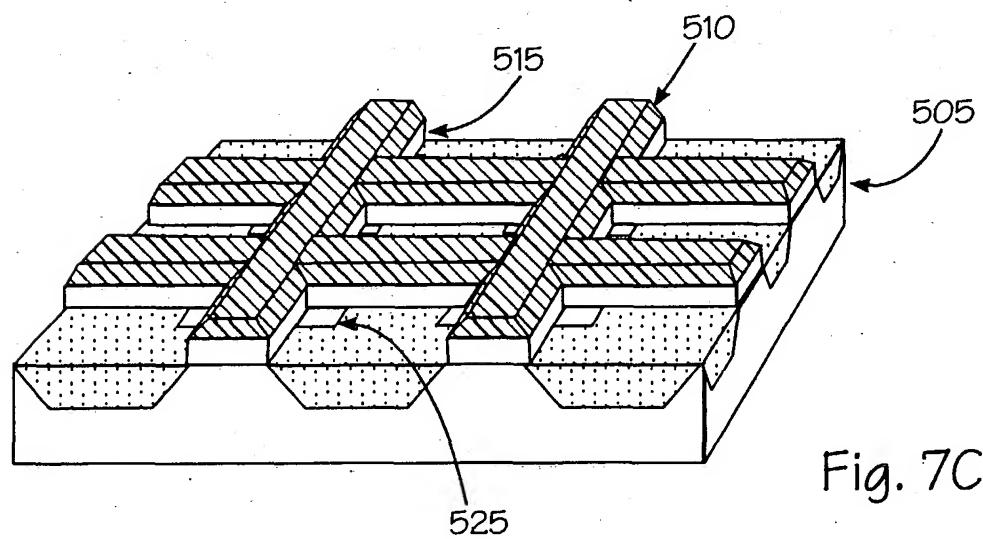
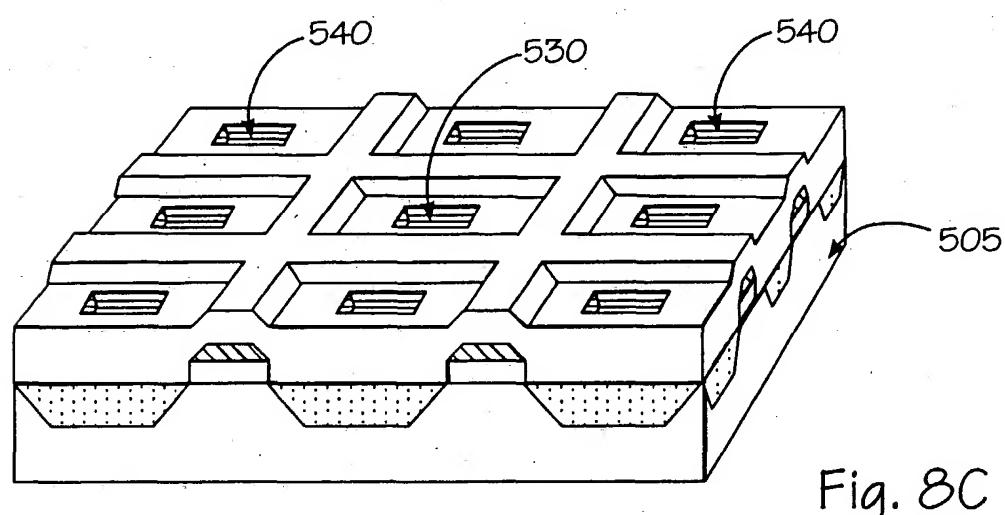
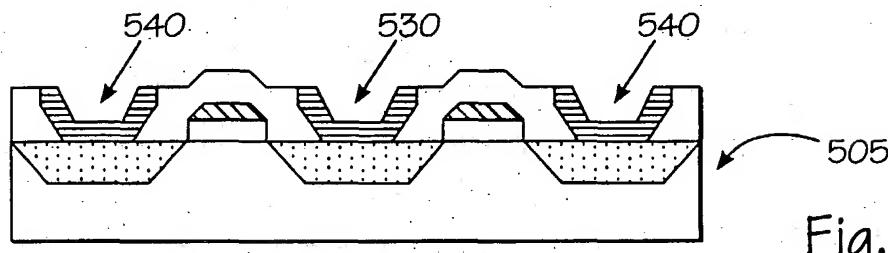
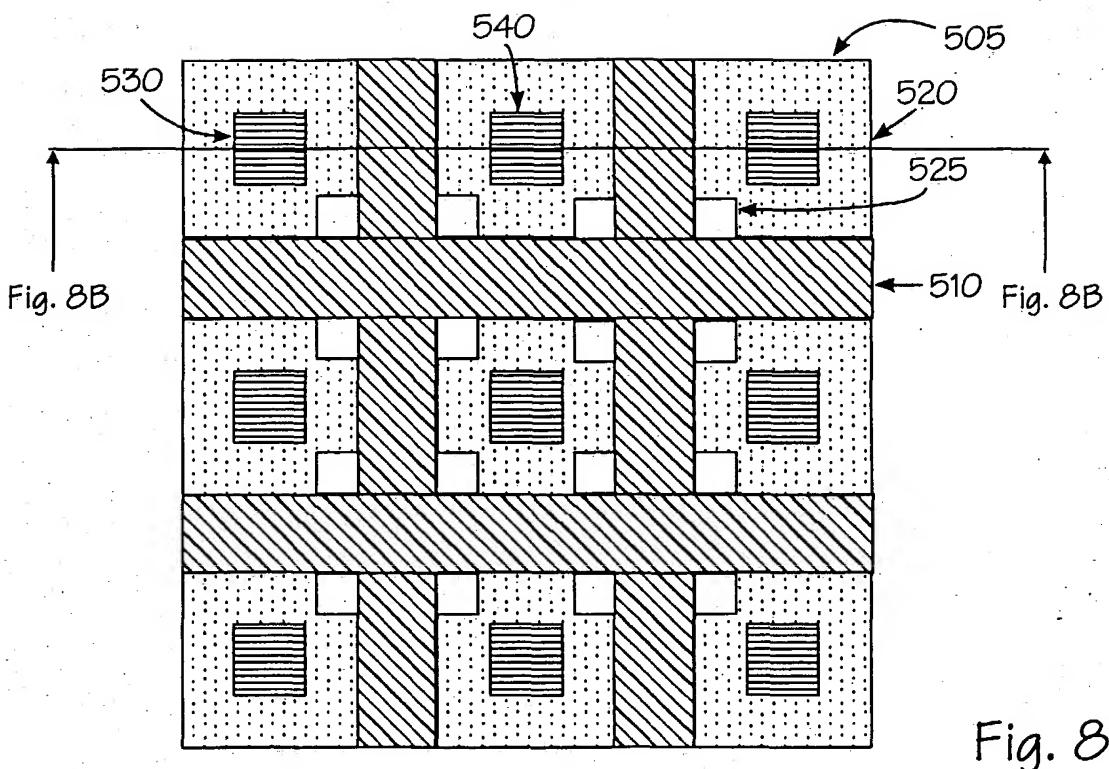


Fig. 7C



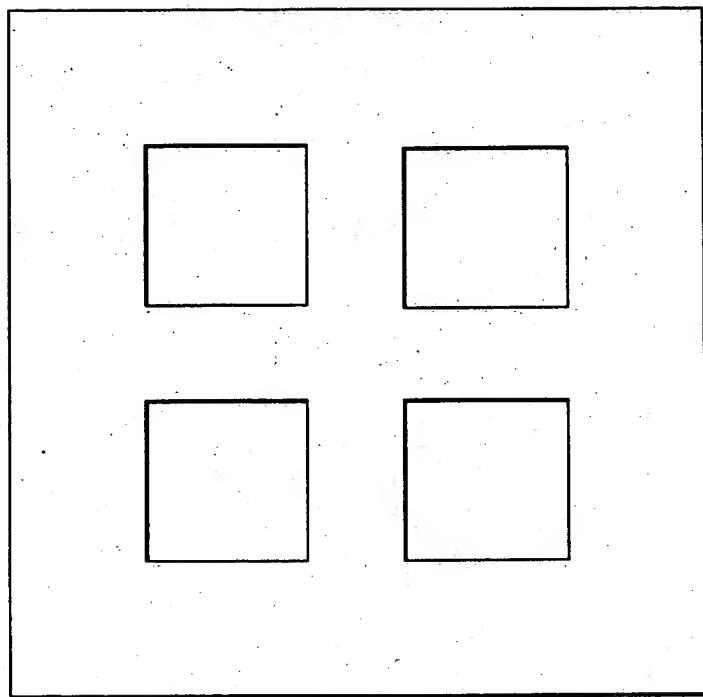


Fig. 9